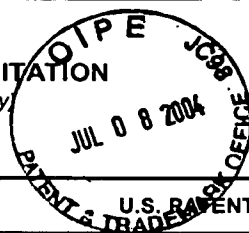


## INFORMATION DISCLOSURE CITATION

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APPLICANT(S)

Kanji OTSUKA et al.

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2631

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## U.S. PATENT APPLICATION PUBLICATIONS

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## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
/TT/	A	11-284126	10/15/1999	JAPAN			✓	
/TT/	B	2000-174505	06/23/2000	JAPAN			✓	
/TT/	C	2002-124635	04/26/2002	JAPAN			✓	

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

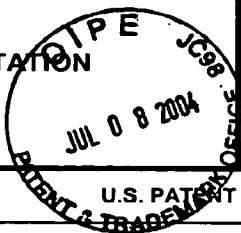
/TT/	D	Kanji Otsuka et al., "Measurement Potential Swing by Electric Filed on Package Transmission Lines," 2001 ICEP Proceedings, pages 490-495.
/TT/	E	Kanji Otsuka et al., "Measurement Evidence of Mirror Potential Traveling on Transmission Lines," The Fifth VLSI Packaging Workshop of Japan, 2000, pages 27 and 28.

EXAMINER	/Thienvu Tran/	DATE CONSIDERED	12/19/2007
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							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/TT/	F	Kanji Otsuka et al., "Stacked Pair Line," JIEP Journal, Vol. 4, No. 7, pp. 556-561 (November 2001).
/TT/	G	Papers at the 15th conference of the Silicon Technology research group of the Japan Applied Physics society in a session Titled "Problems and prospects in ultra-high-speed multilayer interconnect technology," February 18, 2000, University of Tokyo, Yamagami Hall.

EXAMINER	/Thienvu Tran/	DATE CONSIDERED	12/19/2007
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